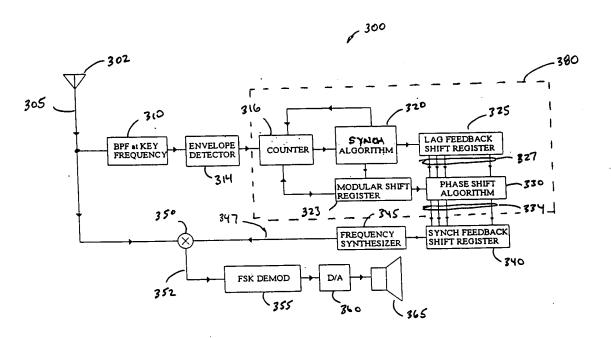
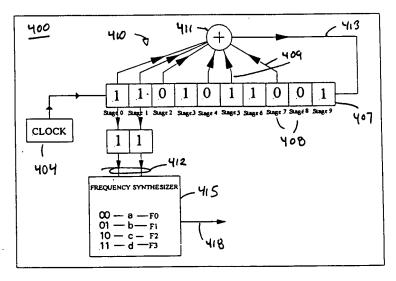


FIG. 2



F16.3

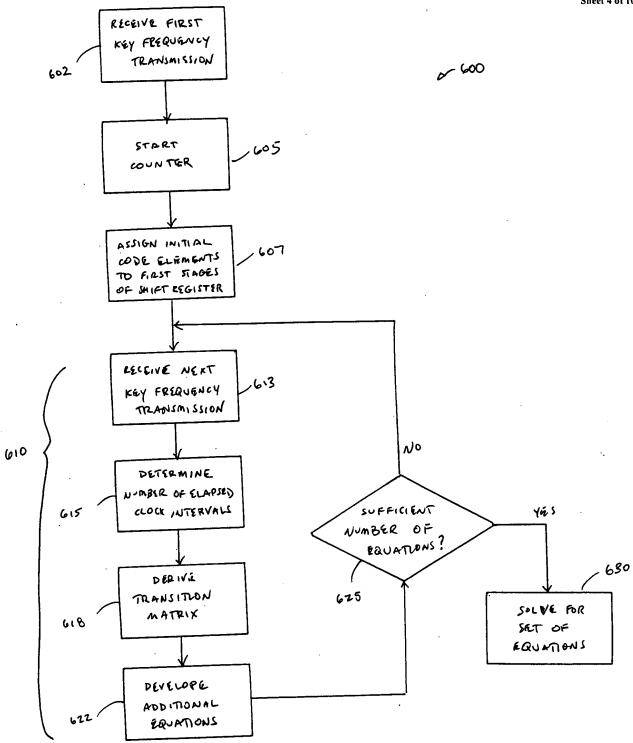


Ten Stage Hopper Configuration

FIG. 4

```
→ 0123456789012345678901234567890... time internals
504 → 110101100110111111111111000110001...
   ddcbcbdcabdcbdddddddddaabdcaab...sequence of frequencies
                        789012
515
               0
                                                  relative times of arrival
              517
                    Code (8) and Associated Sequence of Frequencies
```

FIG. 5



F1G. 6

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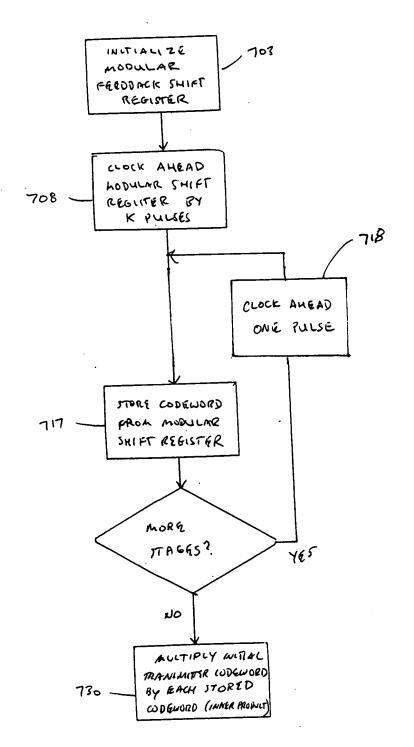
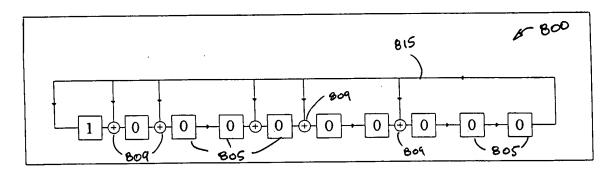


FIG. 7



Ten Stage Modular Register

FIG. 8

FIG. 9

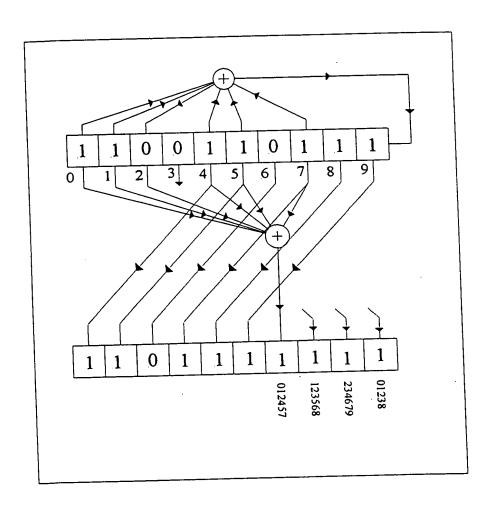
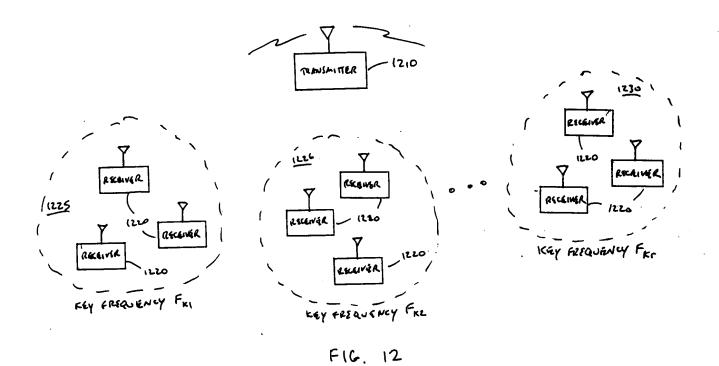


FIG. 10

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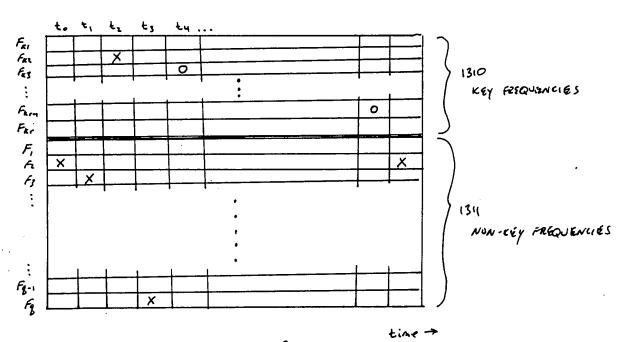


FIG. 13

